

UNITED STATES PATENT AND TRADEMARK OFFICE  
**CERTIFICATE OF CORRECTION**

PATENT NO. : 6,888,732 B2  
APPLICATION NO. : 10/713527  
DATED : May 3, 2005  
INVENTOR(S) : Shane Ching-Feng Hu

Page 1 of 2

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

<u>Column, Line</u>	<u>Reads</u>	<u>Should Read</u>
Item (57), Line 3	“to ground, first and second”	--to ground, and first and second--
Item (57), Line 6	“is coupled”	--are coupled--
Column 1, Lines 25-27	“such as in network- address filtering and translation by matching partial node address. For example, in network router or switch, CAM”	--such as in-network address filtering and translation, by matching partial node addresses. For example, in-network router or switch CAM--
Column 1, Line 60	“to be read sequentially,”	--be read sequentially,--
Column 2, Lines 33-36	“The match circuit discharges a match line in response to a data value stored at the data node to which the match circuit is coupled and compare data present on the respective data line mismatching.”	--The match circuit discharges a match line in response to a data value stored at the data node to which the match circuit is coupled and mismatching data present on the respective data line.--

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Column 2, Lines 39-41	“The capacitive coupling of the first data node assists in is maintained the charge state.”	--The capacitive coupling of the first data node assists in maintaining its charge state.--
Column 4, Lines 25 and 26	“logical level”	--logic level--
Column 4, Line 62	“proceeds as follows.”	--proceeds as follows:--
Column 7, Line 31	“under the right data”	--under the write data--

Signed and Sealed this

Sixth Day of March, 2007



JON W. DUDAS  
*Director of the United States Patent and Trademark Office*